

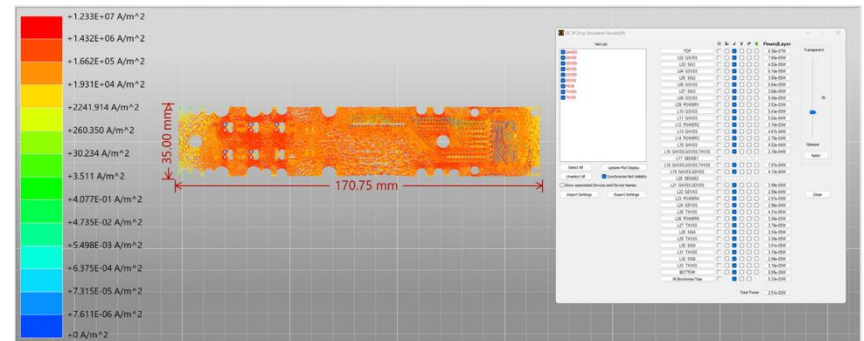
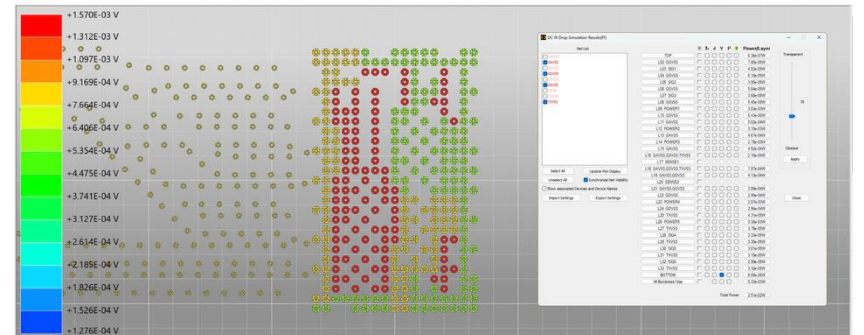
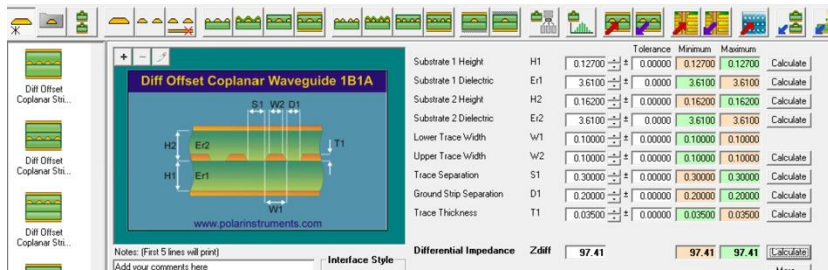
Recent Progress

- **TWEPP Proceeding**

waiting for JINST comments – Alex today told me he is waiting for reviewers feedbacks

- **Mosaix Probe Card**

Good interaction with MPI during the past weeks,
 MLO and PCB designs reviewed and simulated: Power Integrity, Signal Integrity
 All results documented in a Keynote document, examples below:

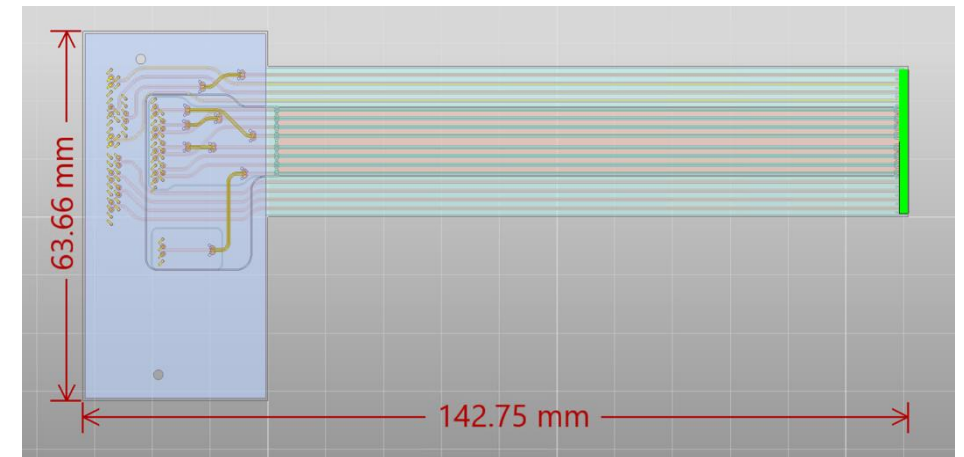
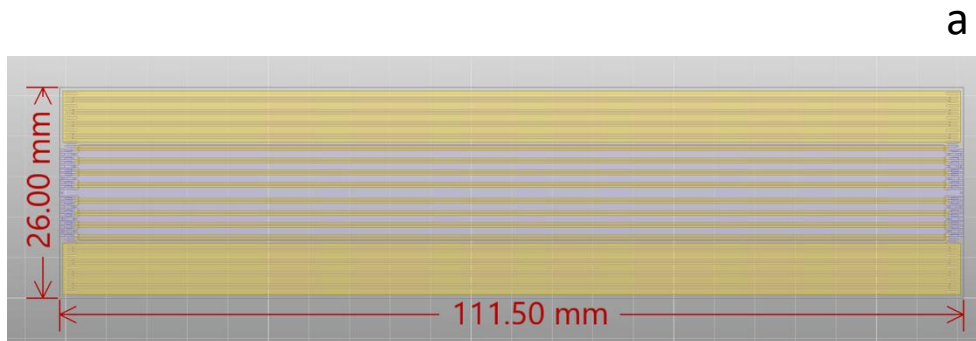


Recent Progress

- **Probe Card FPCs**

When specifying the Probe Card Connector / schematic pinout, we believed we could reuse one of Antoine Flex PCBs, however this is not possible, thus we need design and procure our own to connect the probe card to the Enclustra:

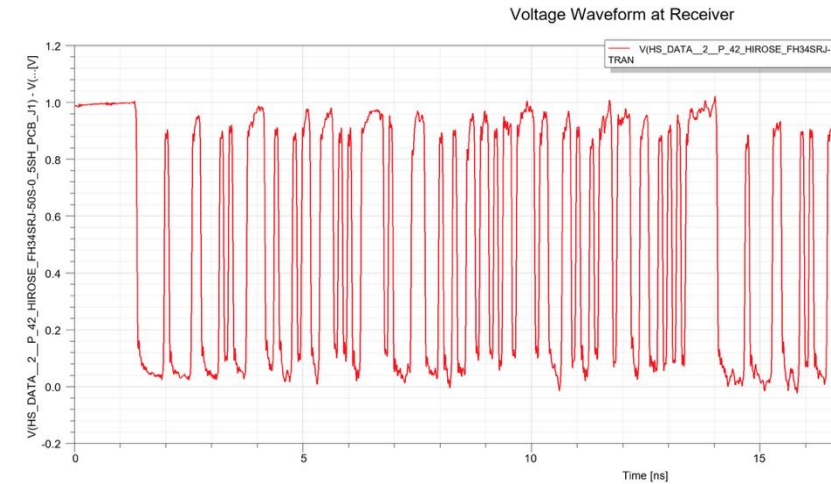
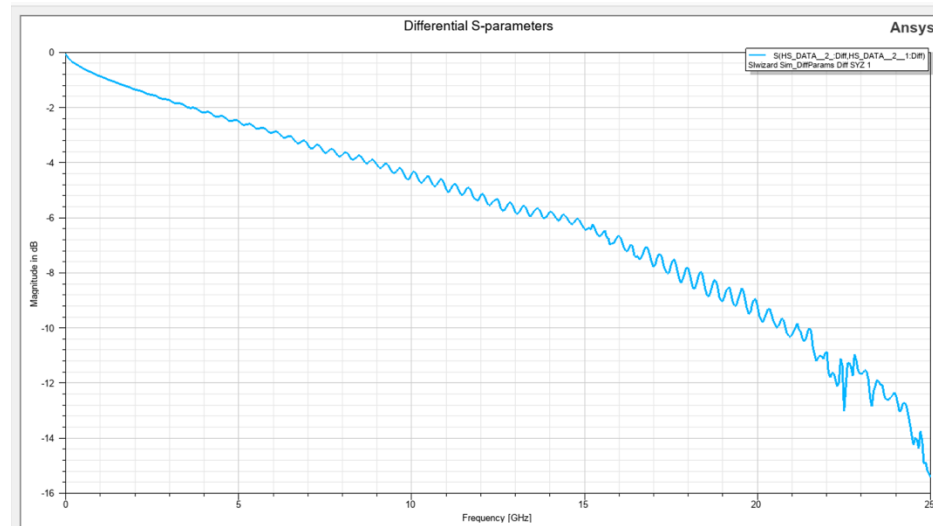
- a. ZIF-ZIF Flex, a simple flex to be used with a PCB from Antoine
- b. ZIF-ZIF Flex, similar to a) , but different pinout for compatibility with the latest ITS3 test system
- c. ZIF-FMC Rigid-Flex a more complex design and stack of materials that would allow to connect directly our probe card to the Enclustra



Several version and lengths designed, a) is in production at CERN thanks to Antoine help with the workshop,
We are waiting for a quotation for b) from a company
Further simulations on S parameters, and transients ongoing

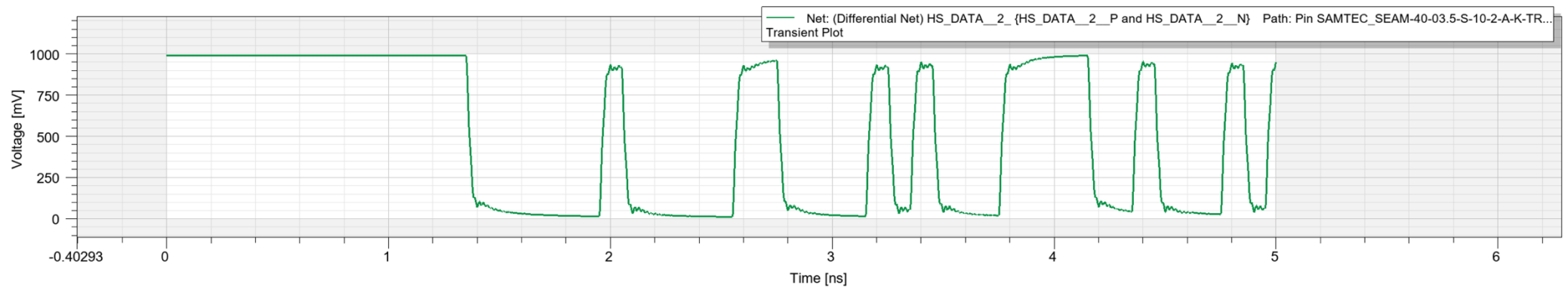
b

Recent Progress



Transient Response Plot

Ansys



Several version and lengths designed, a) is in production at CERN thanks to Antoine help with the workshop,
We are waiting for a quotation for b) from a company
Further simulations on S parameters, and transients ongoing