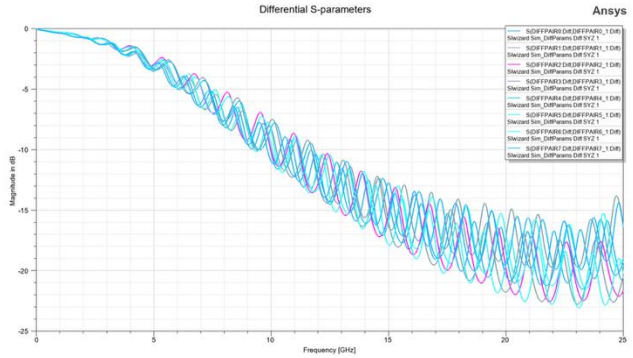
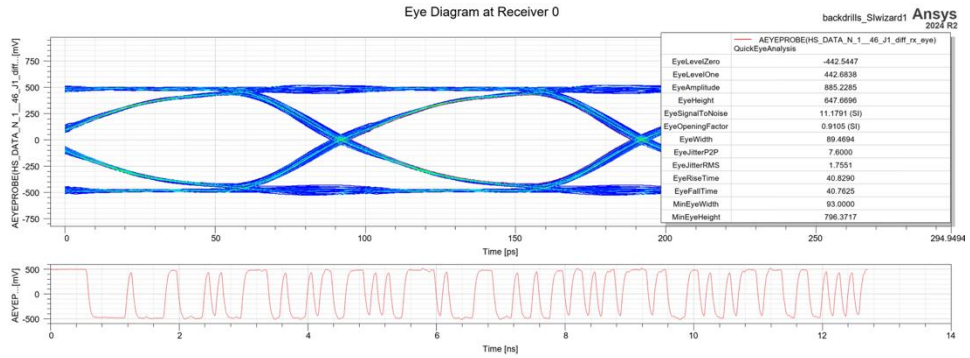
Recent Progress

- **Mosaix Probe Card**
  Further Simulations of the S-parameters and eye diagrams expected from the PCB
  All results documented in a draft Keynote document, examples below:





- **Probe Card FPCs - PCBs**
  7+ versions designed & simulated to optimize signal integrity and have compatibility with ITS3 test systems
  3x version submitted to CERN PCB Workshop should be available around beginning of March
  2x version quoted by ExceptionPCB (UK) – special stackup – plan to start procurement tomorrow
  1x version quoted by Cirly, waiting for Eurocircuit quotations
  1x version still to be designed (breakout board – to be discussed)

- **Initial preparation of the testing setup in DSF, to be continued**