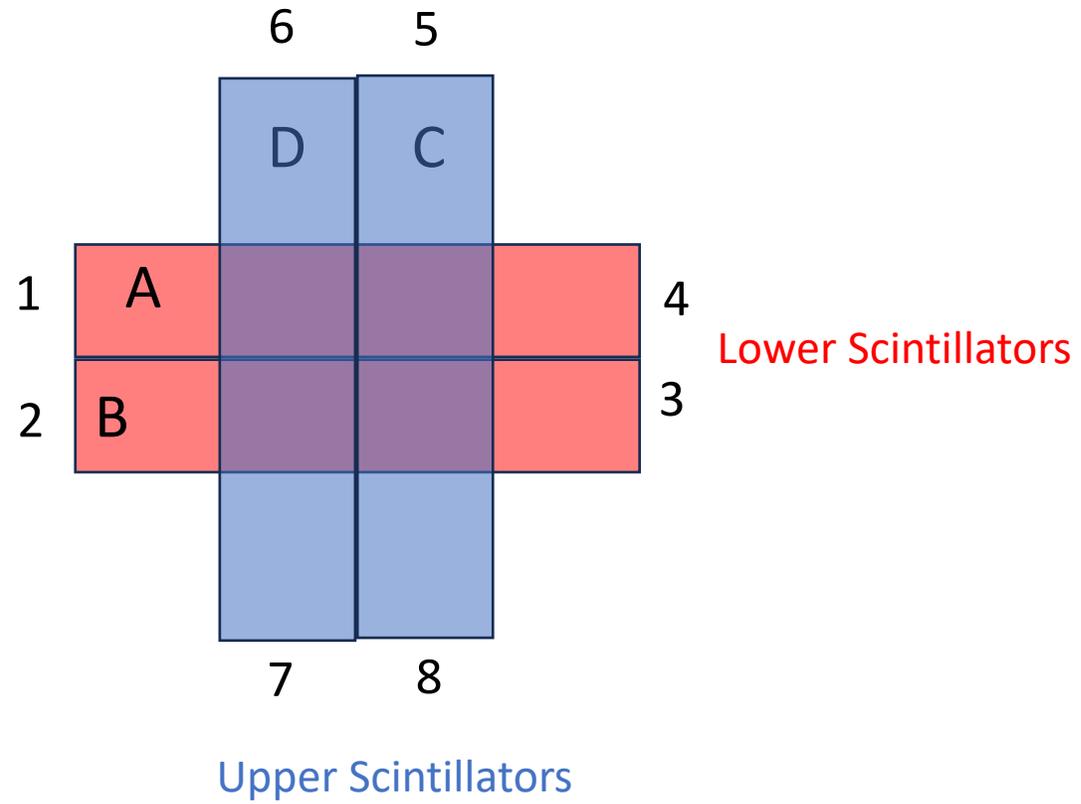
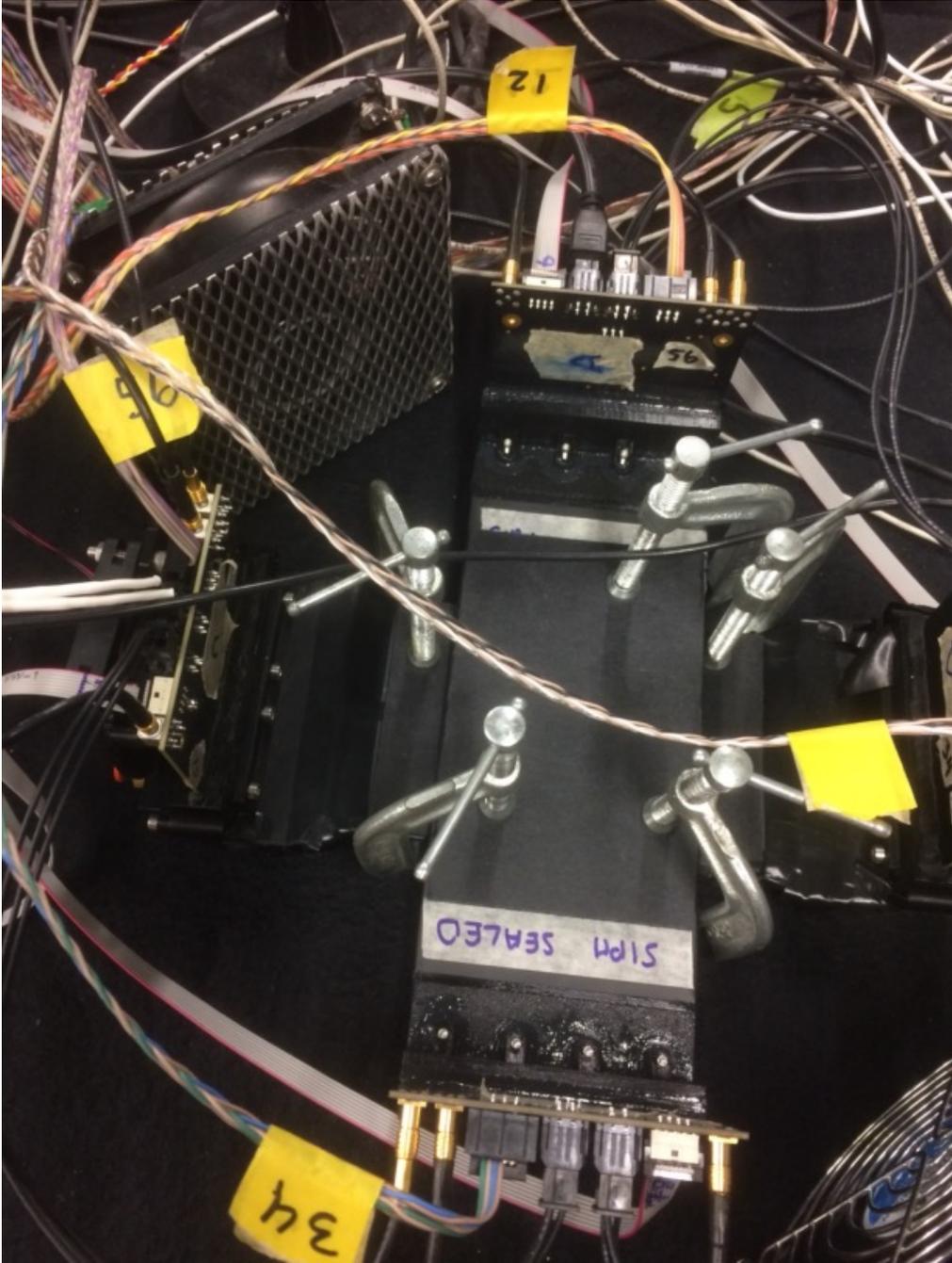


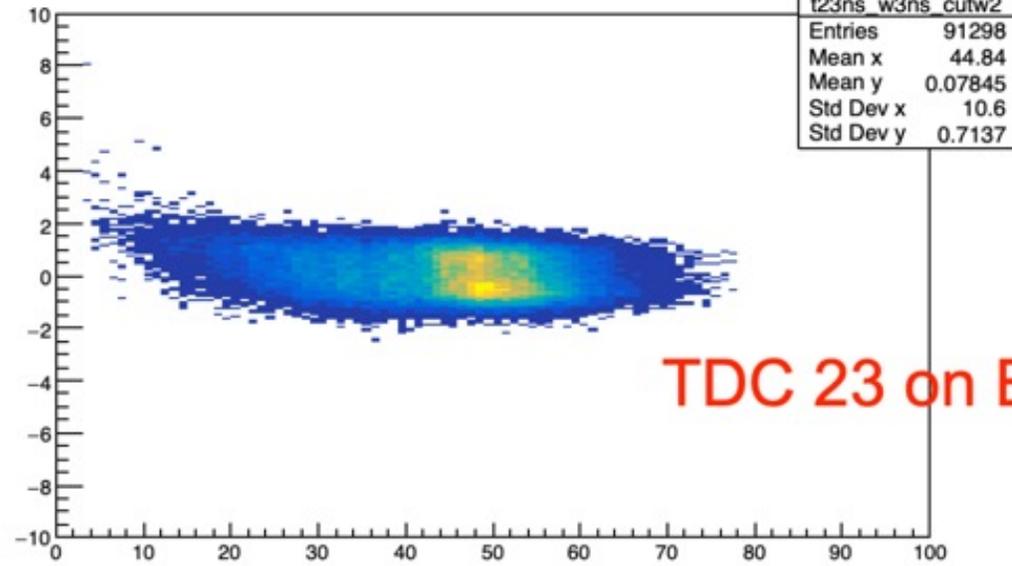
Trigger Scintillator Update Feb 21/24





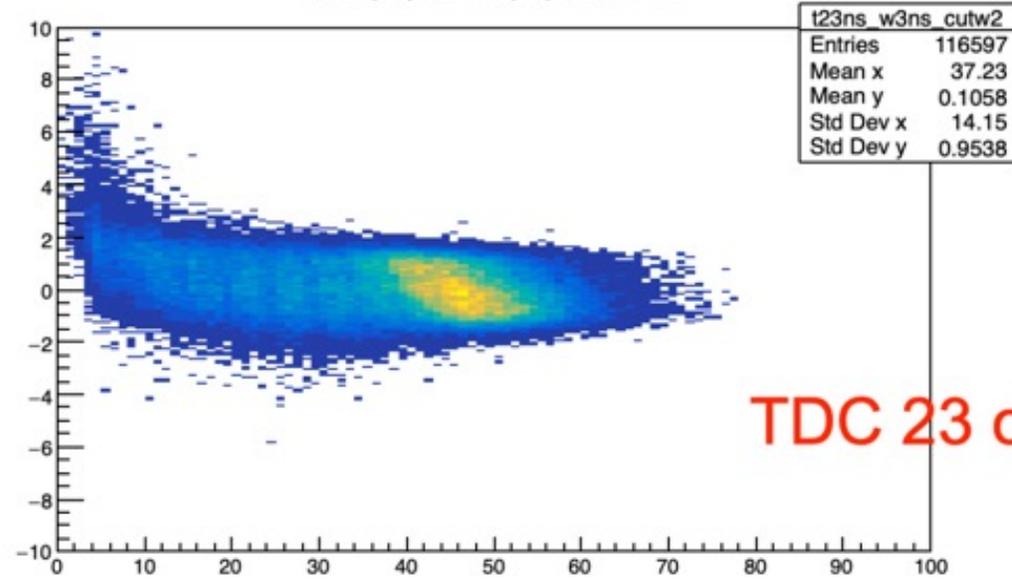
Cables swapped – Feb 9/24
1234 on Top, 5678 on Bottom

t3-t2 (ns) vs w3 (ns) cut on w2



TDC 23 on Bottom scint

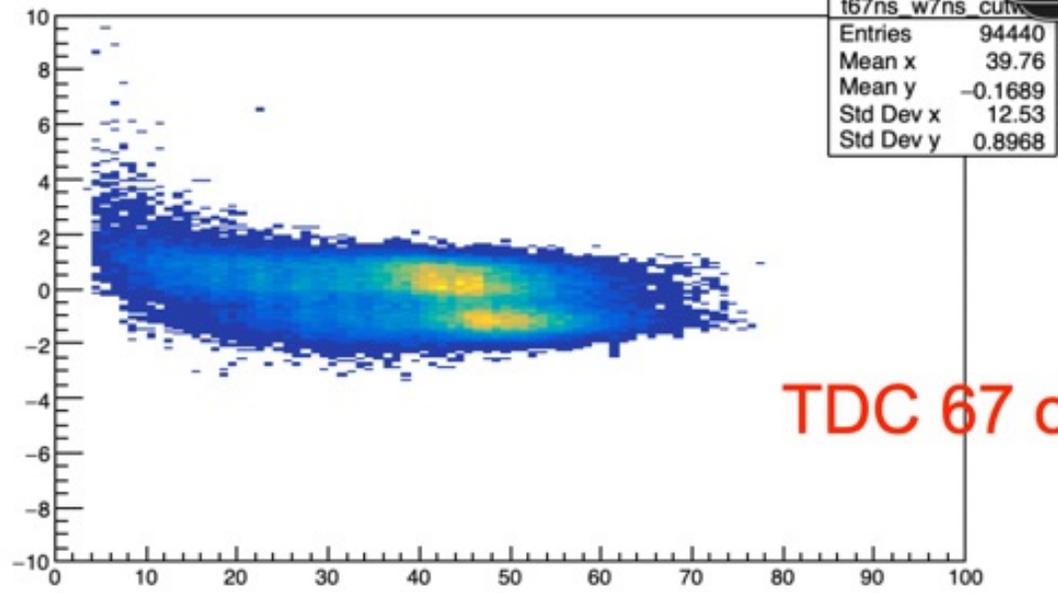
t3-t2 (ns) vs w3 (ns) cut on w2



TDC 23 on Top scint

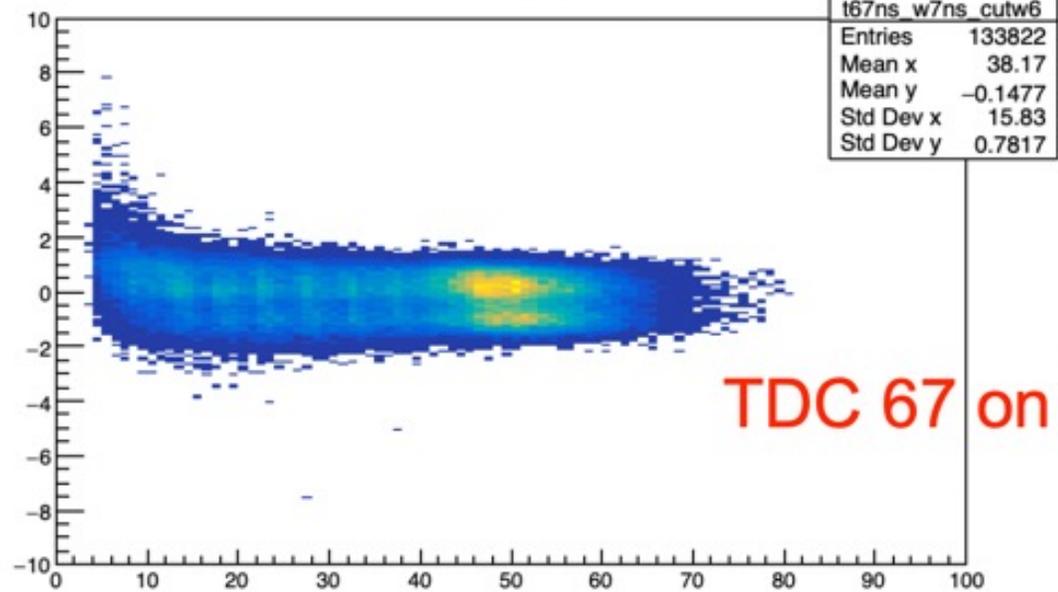
t3-t2 (ns) vs w3 (ns) cut on w2

t7-t6 (ns) vs w7 (ns) cut on w6



TDC 67 on Top scint

t7-t6 (ns) vs w7 (ns) cut on w6



TDC 67 on Bottom scint

t7-t6 (ns) vs w7 (ns) cut on w6

t67ns_w7ns_cutw6

We can now perform a time walk correction on each time value — before we check for a coincidence

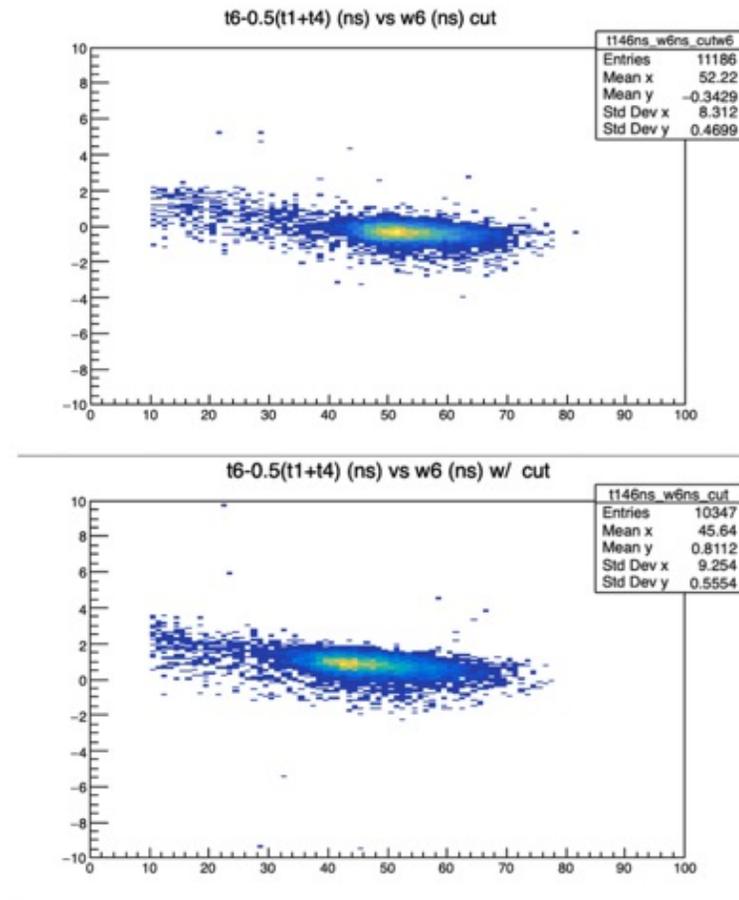


Figure 9: Time difference of $t_6 - \frac{1}{2}(t_1 + t_4)$ versus pulse width (ns). Run 906074 is on the top and 906075 on the bottom.

using the same cable length and same FPGA TDC we can now remove the running TDC value using an average time from the other scintillator for each event and thus perform a time walk correction for each of the 8 TDC L.E. times and improve out coincidence time resolution